

DOCKET NO.: BUR920000228US1

Inrage

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Malinowski et al.)	Examiner: Thai, Luan C.
Serial No.: 09/683,027)	,
Filed: 11/9/01)	Art Unit: 2827
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For: DUAL CHIP STACK METHOD FOR ELECTRO-STATIC DISCHARGE PROTECTION OF INTEGRATED CIRCUITS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Petition Under 37 C.F.R. §1.181(a)

This Petition is in reference to the Notice of Abandonment mailed October 27, 2003.

Applicants request withdrawal of the holding of abandonment for the above-referenced matter.

Enclosed please find a copy of the Response filed on December 9, 2002 with confirmation that the Response was received by the United States Patent and Trademark Office. In addition, enclosed please find a Request for Continued Examination Transmittal.

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